

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Swoboda

Art Unit: 2128

Serial No.: 09/943,595

Confirmation No.: 2423

Filed: August 39, 2001

Examiner: Akash Saxena

Docket: TI-30482

For: COLLECTING AND EXPORTING ON-CHIP DATA PROCESSOR TRACE AND  
TIMING INFORMATION WITH DIFFERING COLLECTION AND EXPORT  
FORMATS

**AMENDMENT UNDER 37 C.F.R. 1.111**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated March 10, 2006. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: